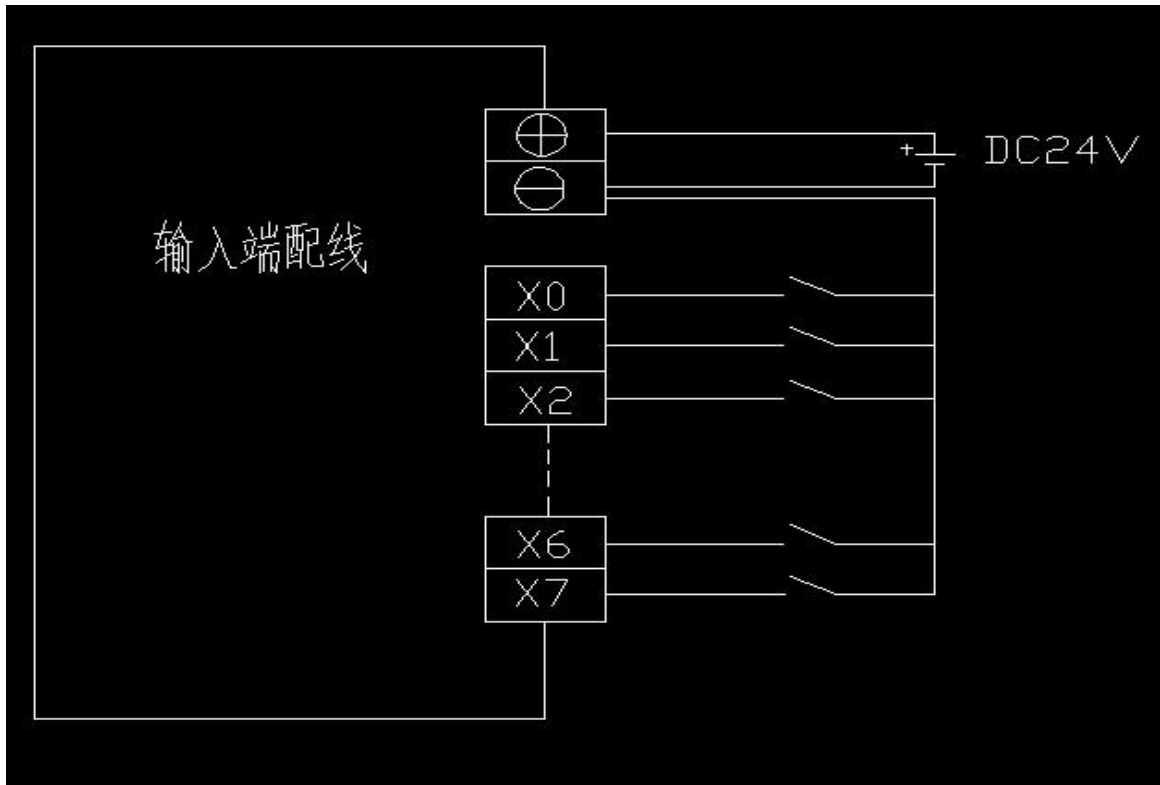
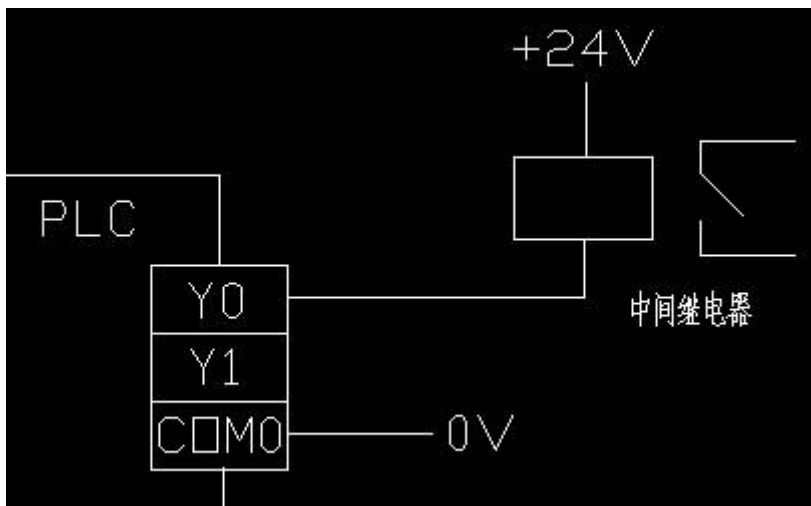


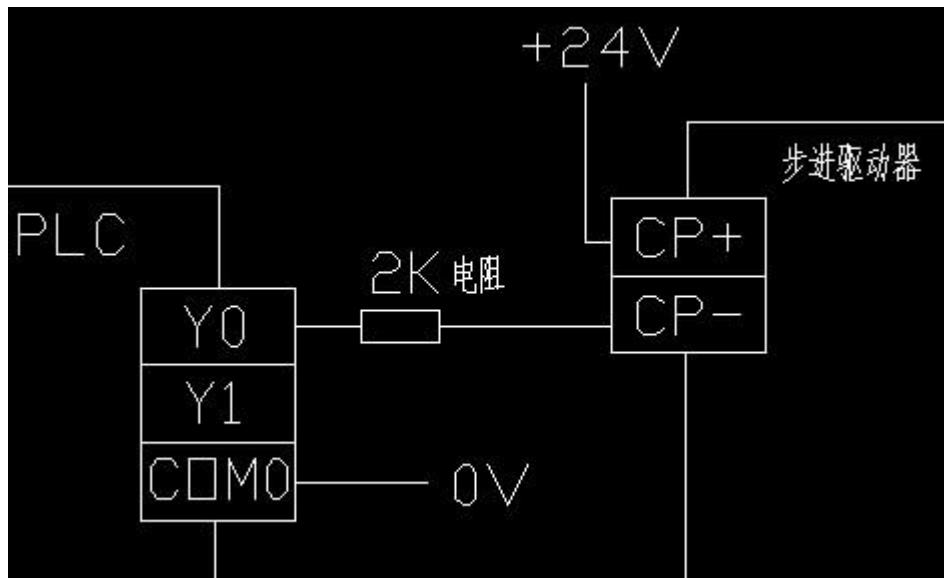
Input wiring:



Output wiring:



Stepper motor wiring:



Programming part

Write applications in ladder language to support Mitsubishi GX-Developer

Support for human machine interface connection, the application is entirely equivalent to Mitsubishi FX1N.

Support ladder programming, download, monitoring.

The programming port is the port for program download and communication with the human-machine interface.

This PLC adopts industrial-grade 32-bit MCU with strong anti-interference, and its running speed far is more than the original Mitsubishi FX1N.

The company's original password protection, as long as the password is 12345678 internal procedures are protected, to prevent your program from being illegally stolen, to protect your labor results

Unlock password: clear PLC memory

Programming language	Ladder diagram, SFC stepping instructions
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Program capacity		8K step EEPROM
Type of instruction	Basic instruction	Sequence control 27, stepping instructions 2
	Application instruction	70
Command speed	Basic instruction	0.1US-0.55US
	Application instruction	0.8US-10US
Input Output	Input relay X	128点 X0-X127
	Output relay Y	128点 Y0-Y127
Auxiliary relay M	General use	1524 点 M0-M1523
	Special use	256点 M8000-M8255

State S		S0-S999
Timer T	100MS	T0-T199
	10MS	T200-T245
	1MS	T246-T249
	100MS accumulation	T250-T255
Counter C 16 bits		C0-C199
Counter C 32 bit		C200-C255
Data regist er	General use	D0-D500
	Indexing	V7-V0、Z7-Z0
	Special use	D8000-D8255
Nested pointer		P0-P127
consta nt	K 16bit	-32768-32767
	K 32 bit	-2 147 483 648-2 147 483 647
	H 16 bit	0-FFFF
	H 32 bit	0-FFFFFFFF

Support instruction set

Basic sequence instruction

Mnemonic, name	Available devices
LD	XYMSTC
LDI	XYMSTC
LDP	XYMSTC
LDF	XYMSTC
AND	XYMSTC
ANI	XYMSTC
ANDP	XYMSTC
ANDF	XYMSTC
OR	XYMSTC
ORI	XYMSTC
ORP	XYMSTC
ANDF	XYMSTC
OUT	YMSTC
SET	YMS
RST	YMSTCD

PLS	YM
PLF	YM
MC	
MCR	
MPS	
MRD	
MPP	
INV	
NOP	
END	

Step ladder instruction

STL Step ladder	Step ladder start	
RET return	Step ladder end	

Application instruction

kind	FNC NO.	Instruction mark
Procedure	00	CJ
	01	CALL

flow	02	SRET
chart	06	FEND
	07	WDT
	08	FOR
	09	NEXT
kind	FNC NO.	Instruction mark
Transf er and compar ison	10	CMP
	11	ZCP
	12	MOV
	15	BMOV
	18	BCD
	19	BIN

kind	FNC NO.	Instruction mark
Arithm etic	20	ADD
	21	SUB
	22	MUL
	23	DIV

	24	INC
	25	DEC
	26	WAND
	27	WOR
	28	WXOR

kind	FNC NO.	Instruction mark
旋转 移位	34	SFTR
	35	SFTL
	38	SFWR
	39	SFRD

kind	FNC NO.	Instruction mark
位处理	40	ZRST
	41	DECO
	42	ENCO

kind	FNC NO.	Instruction mark
高速处理	56	SPD
	57	PLSY
	59	PLSR

kind	FNC NO.	Instruction mark
Convenient order	66	ALT
	67	RAMP
kind	FNC NO.	Instruction mark
Positioning	156	ZRN
	157	PLSV
	158	DRVI
	159	DRVA

kind	FNC NO.	Instructi
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		on mark
Contact comparison instruction	224	LD =
	225	LD >
	226	LD <
	228	LD <>
	229	LD <=
	230	LD >=
	232	AND =
	233	AND >
	234	AND <
kind	FNC NO.	Instructi on mark
Contact comparison instruction	236	AND <>
	237	AND <=
	238	AND >=
	240	OR =
	241	OR >
	242	OR <
	243	OR <>
	244	OR <=

	245	OR >=
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PLC supports 4 channels of 100K high-speed output Y0 Y1 Y2 Y3 Supports relative positioning absolute positioning origin return control The maximum output current of the transistor is 100ma.

1. Because the hardware circuit is different from FX1N, some commands are not supported at the moment, support S480-S991, M500-M1023, D200-D500, C100-C199 power failure maintenance function, temporarily do not support D512-D7999 power failure maintenance function
2. See the FX Series Programming Manual for specific instruction functions.